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10530 U.S. PTO.
09/545648
04/10/00

125	Subclass
710	Class
ISSUE CLASSIFICATION	

PATENT NUMBER
6378020
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U.S. UTILITY Patent Application

O.I.P.E. 3 PATENT DATE APR 23 2002

APPLICATION NO. 09/545648	CONT/PRIOR ID	CLASS 710	SUBCLASS 129	ART UNIT 2701	EXAMINER Stankovic
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APPLICANTS
Michael Farmwald
Mark Horowitz

TITLE
Integrated circuit I/O using a high performance bus interface

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
710	129	365	233		

INTERNATIONAL CLASSIFICATION

G06F	13/00								
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<input type="checkbox"/> TERMINAL DISCLAIMER The term of this patent subsequent to _____ (date) has been disclaimed. The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drawg. <u>14</u> Figs. Drawg. <u>17</u> Print Fig. <u>10</u>			CLAIMS ALLOWED Total Claims <u>41</u> Print Claims for O.G. <u>✓</u>	
	(Assistant Examiner) _____ (Date) _____ Glenn A. Auva Primary Examiner Technology Center 2700			NOTICE OF ALLOWANCE MAILED <u>5/18/02</u>	
	(Primary Examiner) <u>Glenn A. Auva</u> <u>5/17/2001</u> (Date) (Legal Instruments Examiner) <u>Salathia</u> <u>5/23/01</u> (Date)			ISSUE FEE Amount Due <u>\$1240.00</u> Date Paid <u>5-29-01</u>	
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